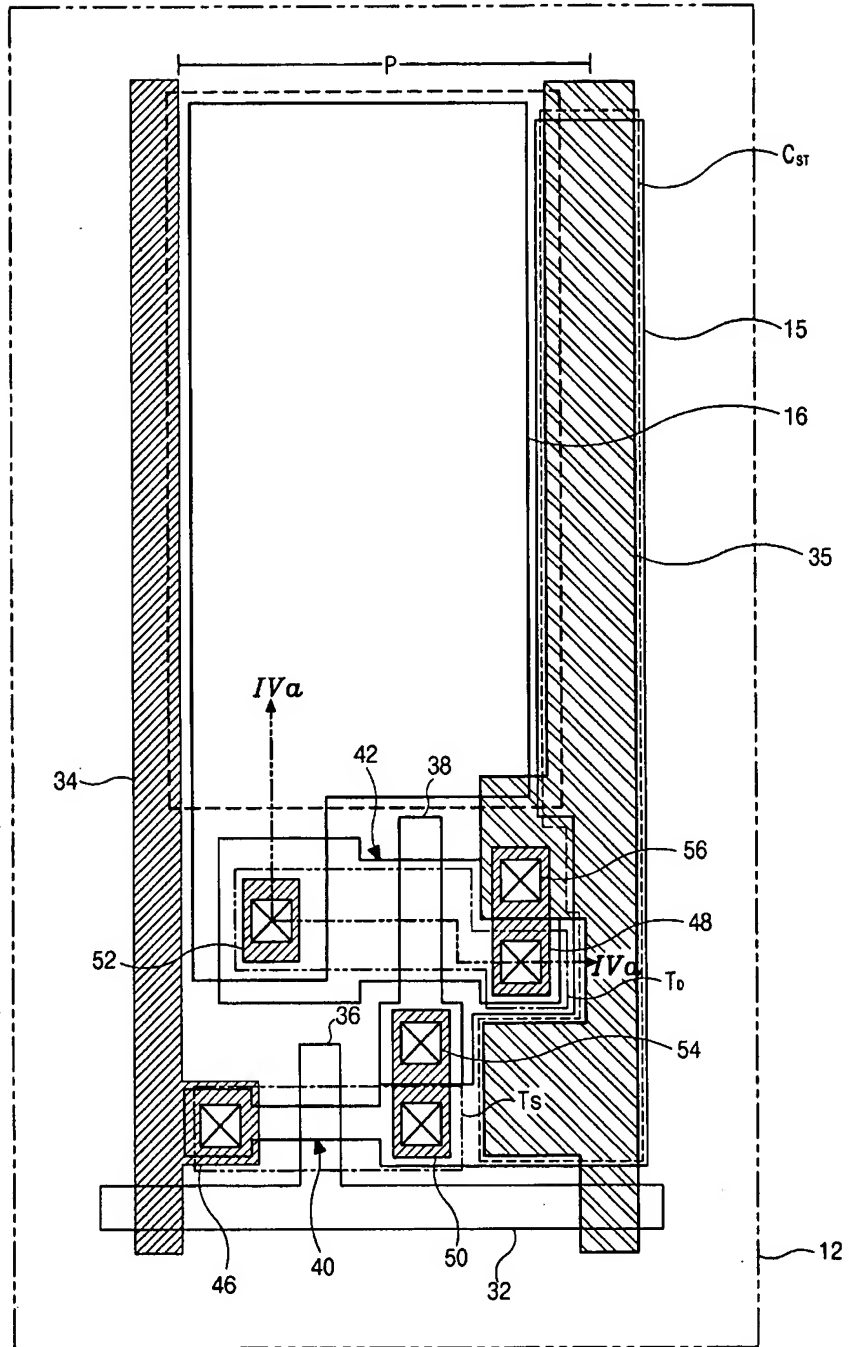
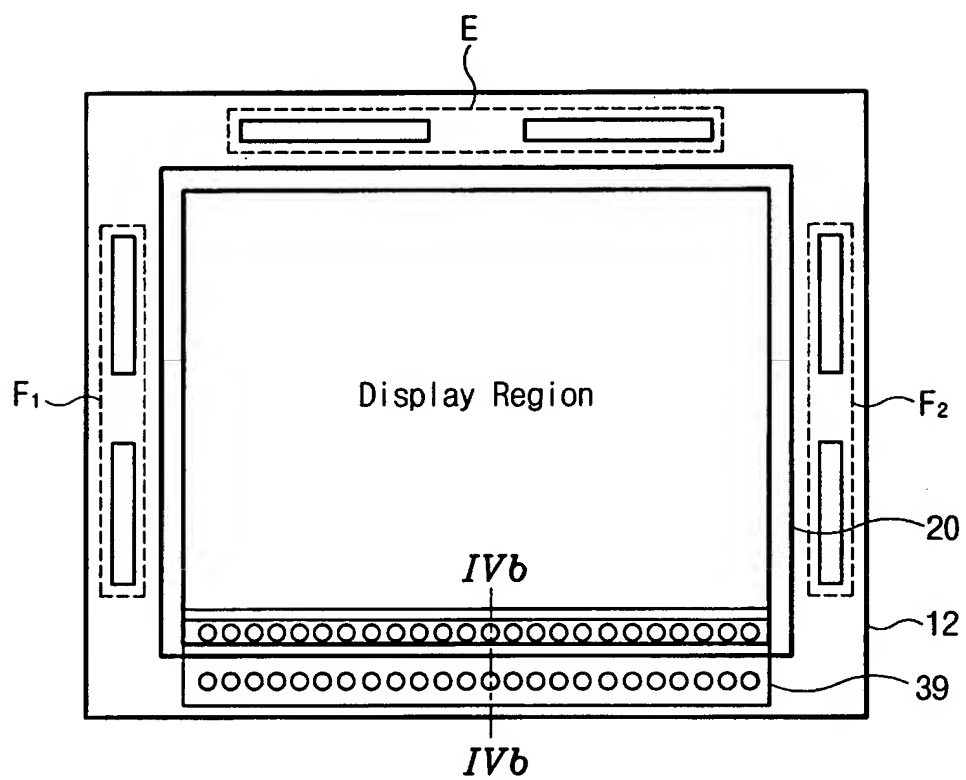


This cross-sectional view shows a display device. A substrate 12 is at the bottom, supporting a pixel array 10. The pixel array consists of a series of subpixels labeled R, G, and B, separated by spacers P. Above the pixel array is a protective layer 14. A top cover 20 is positioned above the pixel array, with a central opening 22. A central protrusion 25 is located within the opening 22. The top cover is supported by side pillars 26. A layer 16 is located between the top cover and the pixel array. A layer 18 is located between the top cover and the pixel array. A layer 28 is located between the top cover and the pixel array.

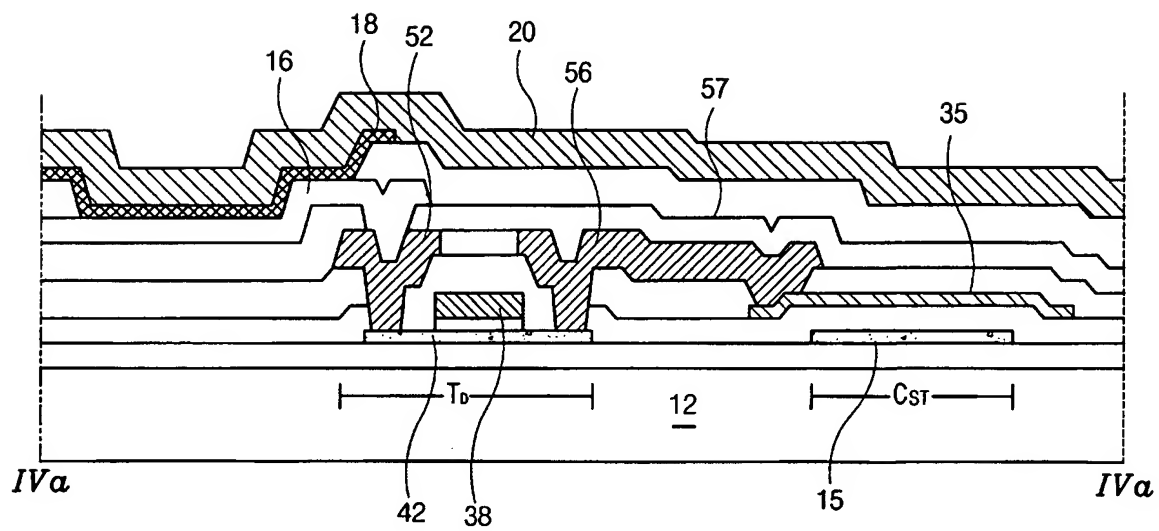
**FIG. 2**  
**Related Art**



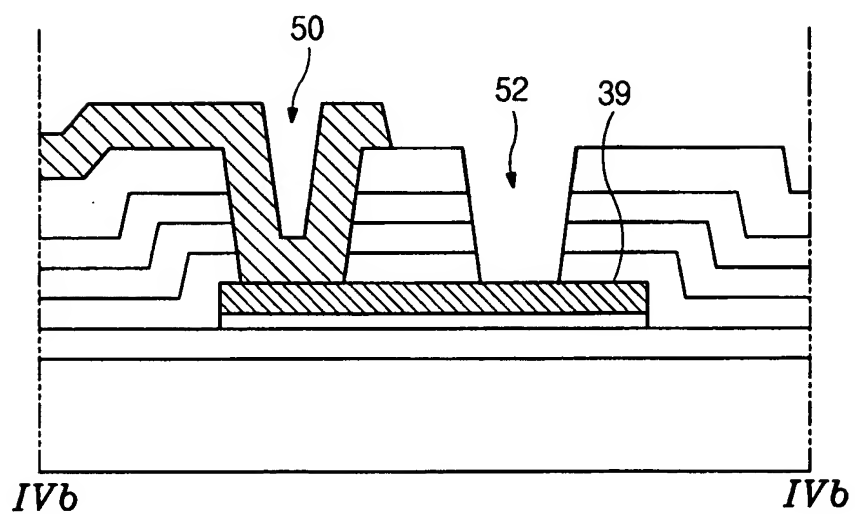
**FIG. 3**  
**Related Art**



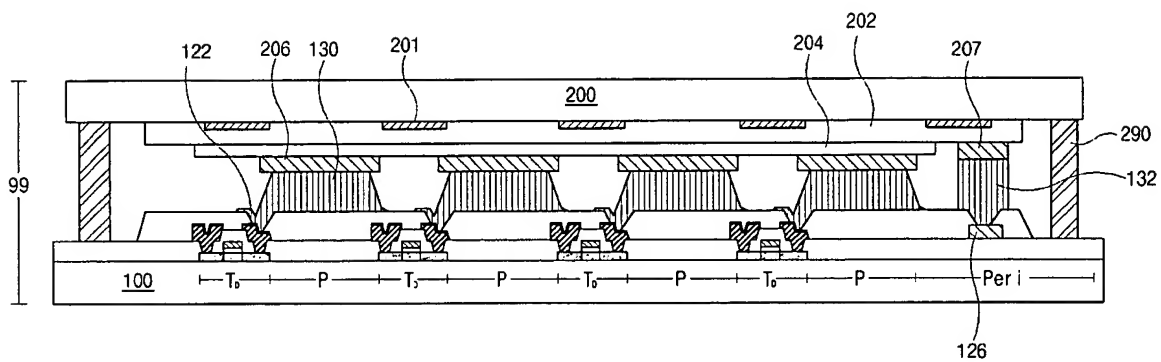
**FIG. 4A**  
**Related Art**



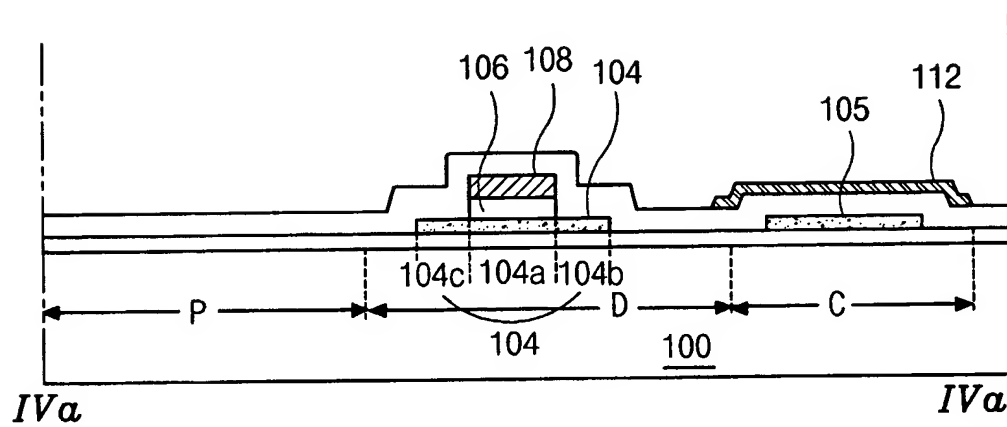
**FIG. 4B**  
**Related Art**



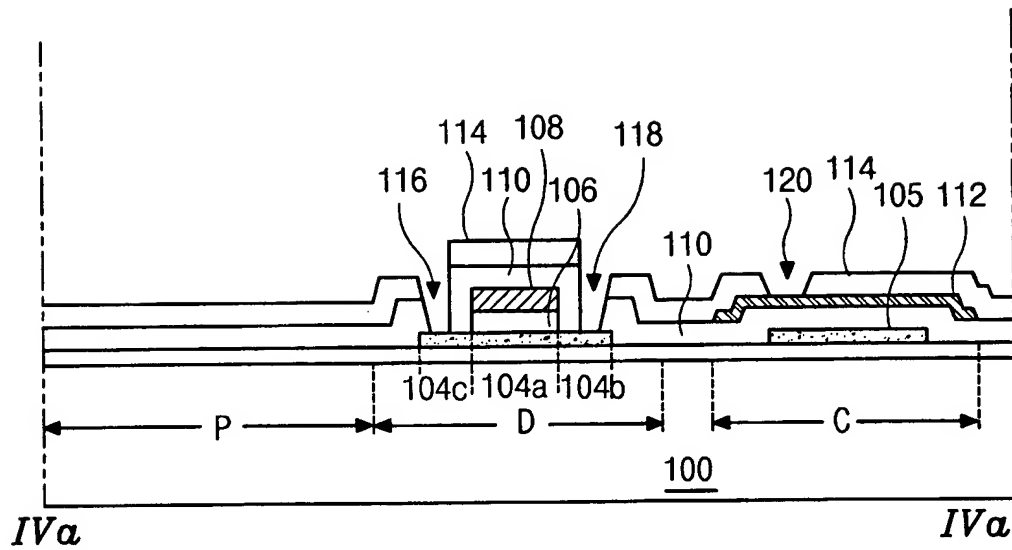
# FIG. 5



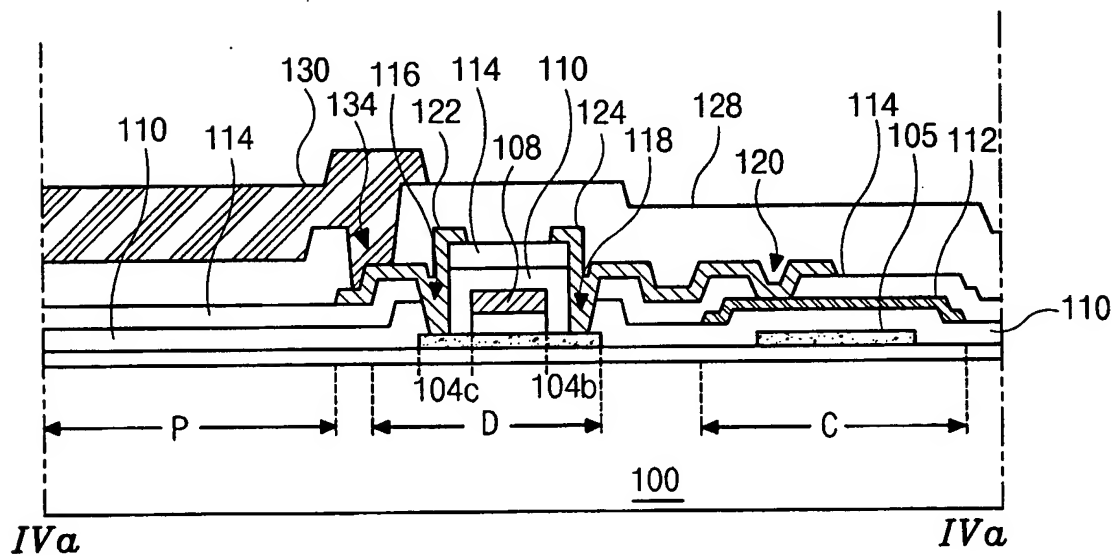
# FIG. 6A



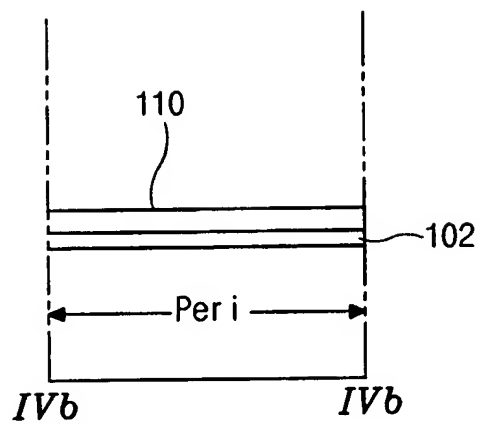
**FIG. 6B**



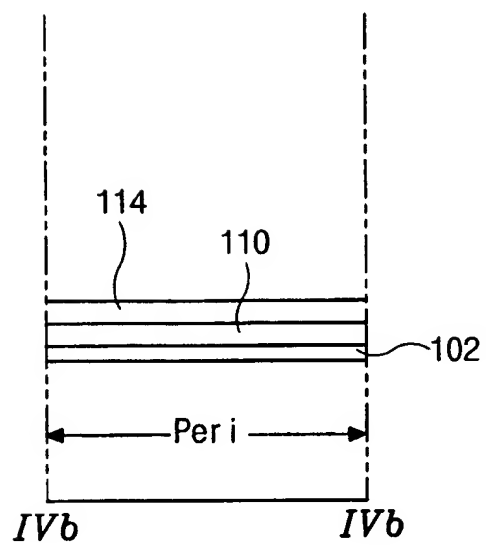
**FIG. 6C**



**FIG. 7A**

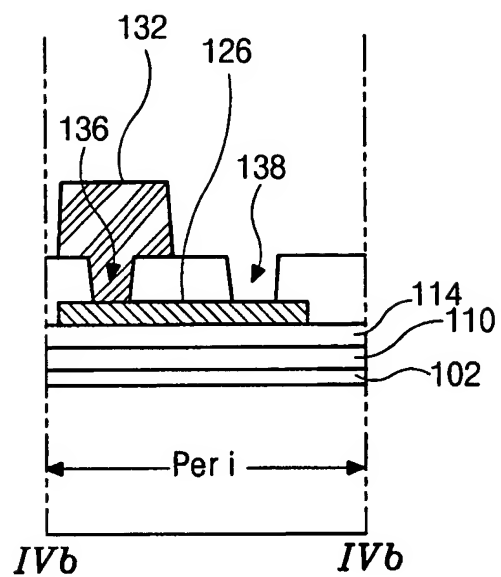


**FIG. 7B**

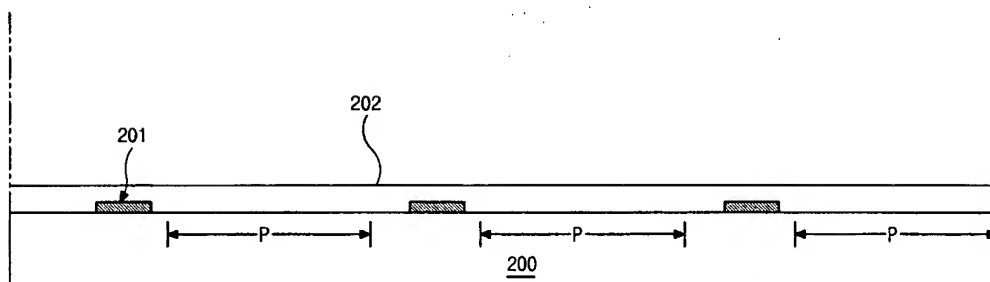




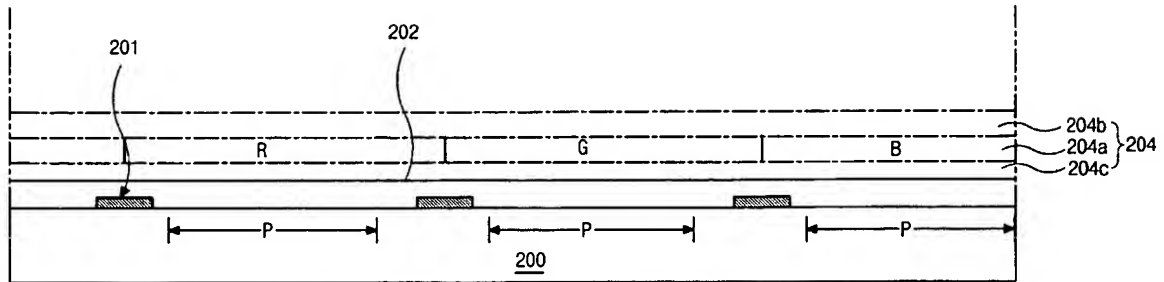
**FIG. 7C**



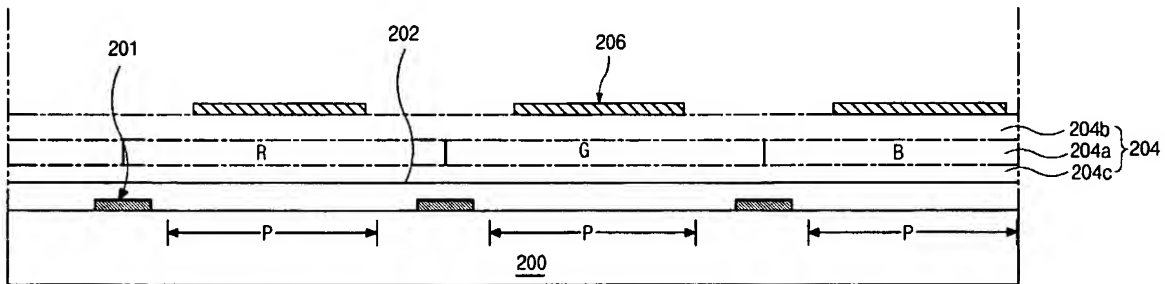
**FIG. 8A**



**FIG. 8B**



**FIG. 8C**



**FIG. 9**

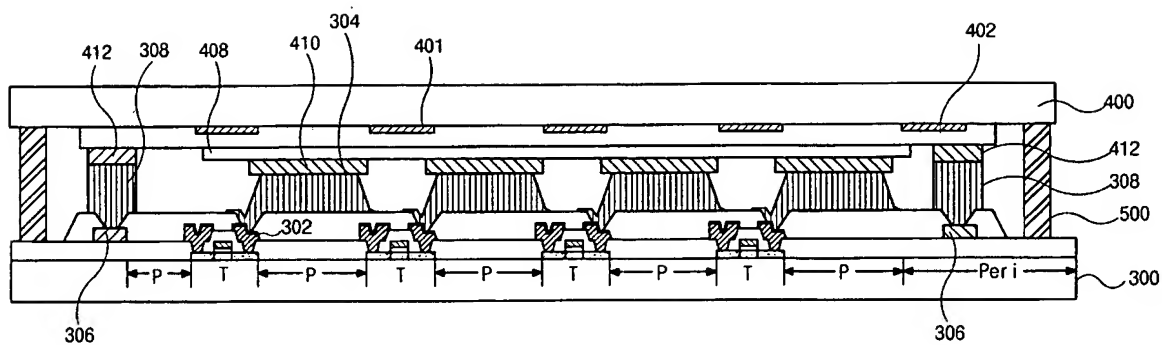


FIG. 10

